

SDRAM

4M x 16 Bit x 4 Banks

FEATURES

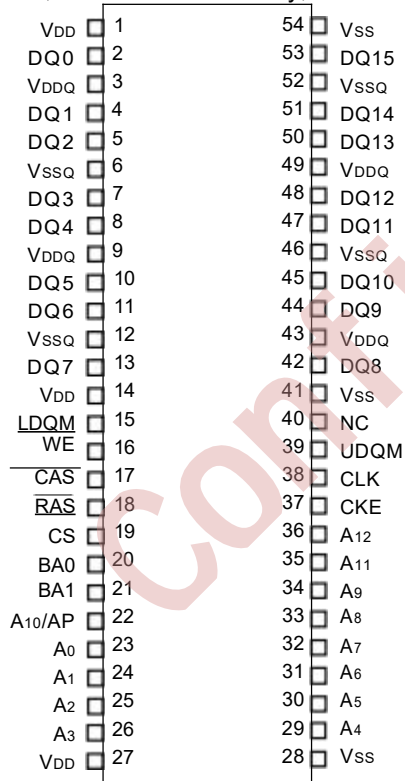
- ☑ JEDEC standard 3.3V power supply
- ☑ LVTTTL compatible with multiplexed address
- ☑ Four banks operation
- ☑ MRS cycle with address key programs
 - CAS Latency (2 & 3)
 - Burst Length (1, 2, 4, 8 & full page)
 - Burst Type (Sequential & Interleave)
- ☑ All inputs are sampled at the positive going edge of the system clock
- ☑ Burst Read single write operation
- ☑ DQM for masking
- ☑ Auto & self refresh
- ☑ 64ms refresh period (8K cycle)
- ☑ All Pb-free products are RoHS-Compliant

ORDERING INFORMATION

Product ID	Max Freq.	Package	Comments
CT51V16M1601A-5S	200MHz	TSOP II	Pb-free
CT51V16M1601A-6S	166MHz	TSOP II	Pb-free
CT51V16M1601A-7S	143MHz	TSOP II	Pb-free
CT51V16M1601A-5SI	200MHz	BGA	Pb-free
CT51V16M1601A-6SI	166MHz	BGA	Pb-free
CT51V16M1601A-7SI	143MHz	BGA	Pb-free

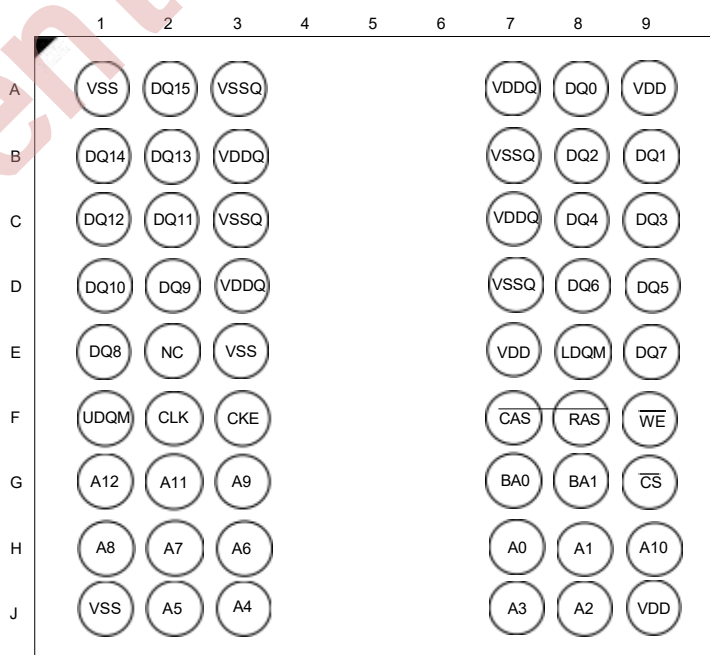
PIN CONFIGURATION (TOP VIEW)

(TSOPII 54L, 400milX875mil Body, 0.8mm Pin Pitch)



BALL CONFIGURATION (TOP VIEW)

(BGA54, 8mmX8mmX1mm Body, 0.8mm Ball Pitch)



Publication Date: Feb. 2015

Revision: 1.4

如需获取完整版规格书
请联系我司岳经理
电话：18138426570
微信二维码如下：



Linda

