

8Gb DDR4 SDRAM

***Lead-Free&Halogen-Free
(RoHS Compliant)***

CT52V2GM4C4-xx

CT52V1GM8C4-xx

CT52V512M16C4-xx

Confidential

Revision History

Revision No.	History	Draft Date	Remark
0.1	Initial Release	Jul. 2017	
1.0	IDD spec update & Changed features (CS released for 2666 SDP)	Sep. 2017	
1.1	Speed bin table note update	Apr. 2018	
1.2	added feature & tREFI and tRFC parameters	Apr. 2018	
1.3	Define IDD specification (2933)	Jun. 2018	
1.4	Correct data (Table 12)	Oct. 2018	
1.5	Define IDD Specification (3200)	Mar. 2019	

如需获取完整版规格书
请联系我司岳经理
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Linda

